

In the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims

1-15. (Cancelled)

16. (Currently amended) A method of forming a plurality of metal bumps, comprising:
providing a chip whose surface comprises a plurality of metal pads;
forming a photoresist layer on the chip;
performing a first etching process to removing remove the photoresist layer that covers the surface and periphery of the metal pad so as to form a first hole that exposes the metal pad;
depositing an insulating layer on the chip to fill the first hole;
performing a second etching process to remove the insulating layer positioned on the surface of the metal pad and remain to leave the insulating layer positioned on the sidewall of the first hole, and thereby a second hole is formed;
filling the second hole with a metal layer; and
removing the remaining photoresist layer.

17. (Original) The method of claim 16, wherein the metal layer is made of Au.

18. (Original) The method of claim 16, wherein the insulating layer is made of silicon oxide or silicon nitride.

19. (Original) The method of claim 16, wherein the metal bump is used for connecting the chip with a nonconducting substrate and the space between two adjacent metal bumps is filled with an anisotropic conductive film (ACF).